In re Patent Application of:
SCHNEIDER ET AL
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IN THE SPECIFICATION:

Attached please find page 24, which was inadvertently missing from the application as originally filed.

The description on page 24 is believed to be self-evident from the illustration in Figure 5 and the attendant description thereof, both in the detailed description and the description beneath the heading "Summary of the Invention". No new matter is involved.

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present application, and the disclosure of which is herein incorporated.

The signal processing functionality of a network (central office (C)) site ATM transceiver 422-C installed
5 within the ALE-C 120 at the network site 100 is shown in the block diagram of Figure 5, while that of a similar customer site ATM transceiver 422-R within the ALE-R 220 at the customer site 200 is shown in the block diagram of Figure 6. The signal processing architectures of ATM
10 transceivers 422-C and 422-R are the same; however, as their respective operational (data rate) parameters are defined by the asymmetrical downlink and uplink communication properties of the link, each ATM transceiver will be described separately.

15 Considering first the network site ATM transceiver 422-C of Figure 5, its signal flow path in the 'to the SDSL link' or downstream direction includes a cascaded arrangement of a CELLDELIN_ATM operator or block 431, an ATMFIFO_2CELL block 433, and a GENCELLS_ATM block 435. In
20 the 'from the SDSL link' or upstream direction, the signal flow path through the ATM transceiver 422-C includes a similar cascaded arrangement of a CELLDELIN_ATM block 441, an ATMFIFO_2CELL block 443, and a GENCELLS_ATM block 445. Each of these blocks, which are
25 preferably implemented in an FPGA, as described above, performs conventional signal processing functions to be described.

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